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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
09/468,015	12/20/1999	DIETMAR EGGERT	F71989US	3122
23720	7590	11/08/2005	EXAMINER	
WILLIAMS, MORGAN & AMERSON, P.C. 10333 RICHMOND, SUITE 1100 HOUSTON, TX 77042			ART UNIT	PAPER NUMBER

DATE MAILED: 11/08/2005

Please find below and/or attached an Office communication concerning this application or proceeding.

Notification of Non-Compliant Appeal Brief (37 CFR 41.37)	Application No. 09/468,015	Applicant(s) EGGERT ET AL	
	Examiner Tanh Q. Nguyen	Art Unit 2182	

--The MAILING DATE of this communication appears on the cover sheet with the correspondence address--

The Appeal Brief filed on 22 August 2005 is defective for failure to comply with one or more provisions of 37 CFR 41.37.

To avoid dismissal of the appeal, applicant must file an amended brief or other appropriate correction (see MPEP 1205.03) within **ONE MONTH or THIRTY DAYS** from the mailing date of this Notification, whichever is longer.

EXTENSIONS OF THIS TIME PERIOD MAY BE GRANTED UNDER 37 CFR 1.136.

1. ☒ The brief does not contain the items required under 37 CFR 41.37(c), or the items are not under the proper heading or in the proper order.
2. ☒ The brief does not contain a statement of the status of all claims, (e.g., rejected, allowed, withdrawn, objected to, canceled), or does not identify the appealed claims (37 CFR 41.37(c)(1)(iii)).
3. ☐ At least one amendment has been filed subsequent to the final rejection, and the brief does not contain a statement of the status of each such amendment (37 CFR 41.37(c)(1)(iv)).
4. ☒ (a) The brief does not contain a concise explanation of the subject matter defined in each of the independent claims involved in the appeal, referring to the specification by page and line number and to the drawings, if any, by reference characters; and/or (b) the brief fails to: (1) identify, for each independent claim involved in the appeal and for each dependent claim argued separately, every means plus function and step plus function under 35 U.S.C. 112, sixth paragraph, and/or (2) set forth the structure, material, or acts described in the specification as corresponding to each claimed function with reference to the specification by page and line number, and to the drawings, if any, by reference characters (37 CFR 41.37(c)(1)(v)).
5. ☐ The brief does not contain a concise statement of each ground of rejection presented for review (37 CFR 41.37(c)(1)(vi)).
6. ☐ The brief does not present an argument under a separate heading for each ground of rejection on appeal (37 CFR 41.37(c)(1)(vii)).
7. ☐ The brief does not contain a correct copy of the appealed claims as an appendix thereto (37 CFR 41.37(c)(1)(viii)).
8. ☐ The brief does not contain copies of the evidence submitted under 37 CFR 1.130, 1.131, or 1.132 or of any other evidence entered by the examiner **and relied upon by appellant in the appeal**, along with a statement setting forth where in the record that evidence was entered by the examiner, as an appendix thereto (37 CFR 41.37(c)(1)(ix)).
9. ☐ The brief does not contain copies of the decisions rendered by a court or the Board in the proceeding identified in the Related Appeals and Interferences section of the brief as an appendix thereto (37 CFR 41.37(c)(1)(x)).
10. ☒ Other (including any explanation in support of the above items):

See Continuation Sheet.

Continuation of 10. Other (including any explanation in support of the above items):

Appellant on page 11 argued that the brief was filed under 37 CFR 41.37, which under 37 CFR 41.37(c) requires a summary of the claimed subject matter (not a summary of the invention) - see item 4 above.

The status of the claims is not correct. Appellant also did not identify the appealed claims (i.e. indicating which claims are appealable).


There is no grouping for each of claims 5-8, 11.

Claims 4 and 18 both include the limitation "for generating an inductance for said low pass filter", which is not part of the record. This limitation is not part of the finally rejected claims. Appellant is requested to clarify which amendment introduces this limitation into claims 4 and 18. Appendix A is a marked up copy of the claims amended submitted in an after final amendment filed 09/09/02, which was entered per RCE filed 10/07/02.

Per 37 CFR 41.37(d), if appellant does not file an amended brief within the set time period, or files an amended brief that does not overcome all the reasons for non-compliance stated in the notification, the appeal will stand dismissed.

TQN

11/2/05


KIM HUYNH
PRIMARY EXAMINER
11/04/05

APPENDIX A

1. (Twice Amended) An electrostatic discharge (ESD) protection network, comprising:

an inductor having a plurality of turns in the shape of a coil, the plurality of turns having an inductance; and

a plurality of electrostatic discharge (ESD) clamp devices, each one of said plurality of ESD clamp devices having a parasitic capacitance, said plurality of ESD clamp devices being connected to a corresponding one of said plurality of (the) turns of said inductor, the inductance of said turns and the parasitic capacitance of said ESD clamp devices thereby forming a low pass filter.

4. (Amended) An integrated circuit apparatus having an electrostatic discharge (ESD) protection network, said apparatus comprising:

an integrated circuit substrate;

a first insulation layer over a face of said integrated circuit substrate;

a plurality of conductive layers, each of the plurality of conductive layers in the shape[d] of a coil turn, the coil turn having a first and second end;

a plurality of insulation layers interleaved between the plurality of conductive layers;

a one of said plurality of conductive layers proximate to said first insulation layer and the other ones of said plurality of conductive layers stacked over the one with said plurality of insulation layers interleaved therebetween;

a plurality of vias in the plurality of insulation layers, the plurality of vias connecting adjacent ones of the coil turns of said plurality of conductive layers, thereby forming an inductor coil; and

a plurality of electrostatic discharge (ESD) clamp devices, each one of said plurality of ESD clamp devices having a parasitic capacitance, said plurality of ESD clamp devices being connected to a corresponding one of the [inductor] coil turns of said plurality of conductive layers, thereby forming a low pass filter.

18. (Amended) A method for providing an electrostatic discharge (ESD) protection network, comprising [the steps of]:

forming a plurality of conductive layers and a plurality of insulation layers, wherein said plurality of conductive [of] layers and said plurality of insulation layers are interleaved, wherein each of the conductive layers is formed in the shape[d] of a coil turn [of a coil] having an inductance such that such that each of the coil turns has a first and a second end;

forming a plurality of vias in said plurality of insulation layers, the plurality of vias being located between the ends of adjacent coil turns wherein conductive material is formed in said plurality of vias thereby connecting the first end of one coil turn to the second end of the adjacent coil turn;

providing a plurality of electrostatic discharge (ESD) clamp devices, each one of said plurality of ESD clamp devices having a parasitic capacitance; and

connecting said plurality of ESD clamp devices to a corresponding one of the coil turns
of said plurality of conductive layers, thereby forming a low pass filter.